L Number	Hits	Search Text	DB	Time stamp
1	105425	defective	USPAT; US-PGPUB	2004/08/03 07:47
2	201330	skip$ bypass$	USPAT; US-PGPUB	2004/08/03 07:48
3	1278	defective with (skip$ bypass$)	USPAT; US-PGPUB	2004/08/03 07:48
4	3170525	processor unit device circuit controller	USPAT; US-PGPUB	2004/08/03 07:49
5	656	(defective with (skip$ bypass$)) same (processor unit device circuit controller)	USPAT; US-PGPUB	2004/08/03 07:50
6	433	(defective with (skip$ bypass$)) with (processor unit device circuit controller)	USPAT; US-PGPUB	2004/08/03 07:50
8	29	graphic and ((defective with (skip$ bypass$)) same (processor unit device circuit controller))	USPAT; US-PGPUB	2004/08/03 07:51
7	7	graphic and ((defective with (skip$ bypass$)) with (processor unit device circuit controller))	USPAT; US-PGPUB	2004/08/03 09:06
9	22	(graphic and ((defective with (skip$ bypass$)) same (processor unit device circuit controller))) not (graphic and ((defective with (skip$ bypass$)) with (processor unit device circuit controller)))	USPAT; US-PGPUB	2004/08/03 08:03
10	426	((defective with (skip$ bypass$)) with (processor unit device circuit controller)) not (graphic and ((defective with (skip$ bypass$)) same (processor unit device circuit controller)))	USPAT; US-PGPUB	2004/08/03 09:04
11	119141	fault$	USPAT; US-PGPUB	2004/08/03 09:04
12	1439	(skip$ bypass$) with fault$	USPAT; US-PGPUB	2004/08/03 09:04
13	851	(processor unit device circuit controller) same ((skip$ bypass$) with fault$)	USPAT; US-PGPUB	2004/08/03 09:05
14	571	(processor unit device circuit controller) with ((skip$ bypass$) with fault$)	USPAT; US-PGPUB	2004/08/03 09:06
15	34	graphic and ((processor unit device circuit controller) same ((skip$ bypass$) with fault$))	USPAT; US-PGPUB	2004/08/03 09:06
16	19	graphic and ((processor unit device circuit controller) with ((skip$ bypass$) with fault$))	USPAT; US-PGPUB	2004/08/03 09:06

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